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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,620	10/24/2003	Aiping Huang	CS22507RL	8802
20280	7590	12/01/2005	EXAMINER	
MOTOROLA INC 600 NORTH US HIGHWAY 45 ROOM AS437 LIBERTYVILLE, IL 60048-5343			VU, TRISHA U	
			ART UNIT	PAPER NUMBER
			2112	

DATE MAILED: 12/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/692,620

Applicant(s)

HUANG, AIPING

Examiner

Trisha Vu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on 24 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 6 and 7 is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) 5 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-7 are presented for examination.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 3 and 4 are rejected under 35 U.S.C. 102(e) as being anticipated by Moran et al. (US Pub. No. 2004/0157638) (hereinafter Moran).

As to claim 1, Moran teaches a Universal Serial Bus device (telephone device 108 as shown in Fig. 2A or 920 as shown in Fig. 9) comprising: a first processor (CPU 926) configured as a Universal Serial Bus device to provide a first set of services (e.g. telephone services) to an external host (host computer 102); and a second processor (CPU 924), coupled to said first processor as a Universal Serial Bus host and coupled to a Universal Serial Bus device connector (USB connector 922) suitable for connection to said external host, configured to provide a second set of services (e.g. controlling one or more flash memory modules 906) to said external host, and configured to pass service data bi-directionally between said first processor and said external host for said first set of services (at least Fig. 9 and paragraphs [0105-0109] and [0179-0180]).

As to claim 3, Moran further teaches the USB device further comprising a wireless transceiver (paragraphs [0130] and [0162]).

As to claim 4, Moran further teaches said second processor is further configured to transmit a de-configuration request to said first processor in response to at least one of a physical disconnection of said external host and a service disconnect message from said external host (proper disconnection requires a user to indicate impending disconnection to host 102 and/or telephone 108 (e.g., via the telephone or host interface)) (paragraph [0113]), and wherein said first processor is further configured to cancel at least one service in response to said de-configuration request (this is inherent due to disconnection, the service(s) provided by the first processor is canceled).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Moran et al. (US Pub. No. 2004/0157638) (hereinafter Moran) in view of Lin (6,654,841).

As to claim 2, the argument above for claim 1 applies. However, Moran does not explicitly disclose said first processor is further configured to have a plurality of normally open logical service switches, wherein said first set of services are unavailable to said second processor during bus enumeration from said first processor to said second

processor, and wherein at least one of said plurality of normally open logical service switches will close with respect to at least one of said first set of services in response to a request by said second processor. Lin teaches USB device (Fig. 1) having processor (microcontroller 1) configured to have a plurality of normally open logical service switches (semiconductor switch 4), wherein the processor's services are unavailable to host during bus enumeration (the functions are shifted based on different trigger position) (col. 1 lines 63-65), and wherein at least one of said plurality of normally open logical service switches will close with respect to at least one of said first set of services (col. 2 line 25 to col. 3 line 23). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the processor with logical service switches as taught by Lin in the system of Moran to provide services of flash memory even if no flash memory card is available (col. 1 lines 9-14).

Allowable Subject Matter

4. Claims 6 and 7 are allowed. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Claim 5 includes the limitation of the second processor comparing configuration sets of said first processor and said second processor in response to a set configuration request from said external host by, transmitting a set interface request to said first processor if said configuration sets are different, and transmitting a configuration request to said first processor if said

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configuration sets are identical, which is not shown by the prior art of record, in the combination as disclosed and claimed.

Claim 6 includes the limitation of determining that the first processor and the second processor have the same configuration sets and closing by the first processor a logical switch to connect services to said second processor in response to the configuration request, which is not shown by the prior art of record, in the combination as disclosed and claimed.

Claim 7 includes the limitation of determining that the first processor and the second processor have different configuration sets and closing by the first processor at least one logical switch to connect at least one service to said second processor in response to the set interface request, which is not shown by the prior art of record, in the combination as disclosed and claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure as the art teaches USB device configuration/enumeration:

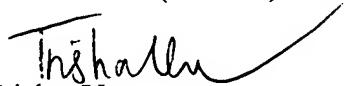
US Patent	6,389,495	Larky et al.
US Patent	6,862,643	Wu et al.
US Patent	6,832,273	Ray et al.


US Patent	6742,055	Matsunaga
US Patent	6,560,665	Resler et al.
US Patent	6,131,125	Rostoker et al.
US Pub. No	2003/0027562	Nishimura
US Pub. No.	2005/0006483	Fruhauf
US Pub. No.	2005/0249143	Tee et al.
US Pub. No.	2004/0193744	Paley et al.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trisha Vu whose telephone number is 571-272-3643. The examiner can normally be reached on Mon-Thur and alternate Fri 8:00am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Trisha Vu
Examiner
Art Unit 2112


REHANA PERVEEN
SUPERVISORY PATENT EXAMINER
11/28/05